

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appln. No: 10/594,161  
Applicant: Tomohiro MURAKOSO et al.  
Filed: September 26, 2006  
Title: PLASMA DISPLAY PANEL  
TC/A.U.: 2889  
Examiner: Elmito Breval

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

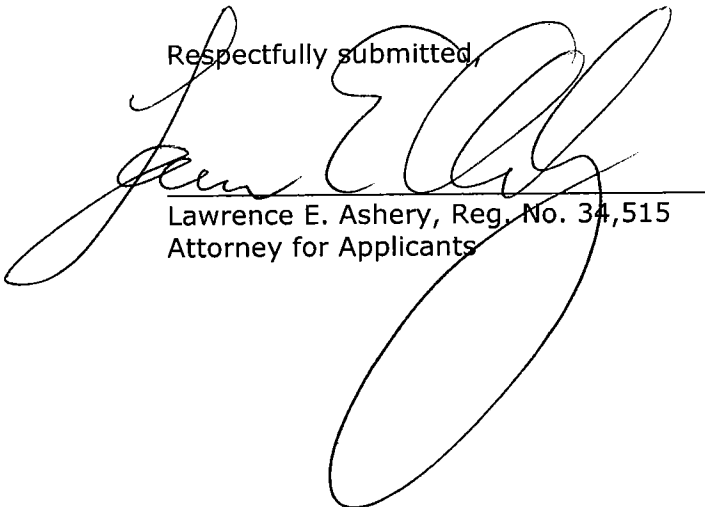
Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and to the duty of disclosure set forth in 37 C.F.R. § 1.56, the Examiner in charge of the above-identified application is requested to place in the file of the above-identified application the references listed on the PTO/SB/08a form submitted herewith. A copy of the references listed on the PTO/SB/08a form is not required to be enclosed because they are U.S. patent documents.

Although the information submitted herewith may be "material" to the Examiner's consideration of the subject application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

This Information Disclosure Statement is being filed after the period provided for in 37 CFR § 1.97(d) but before issuance of a patent. Accordingly, please place the enclosed references in the application file in accordance with 37 CFR § 1.98(i).

Respectfully submitted,



Lawrence E. Ashery, Reg. No. 34,515  
Attorney for Applicants

LEA/dmw

Enclosure: PTO/SB/08a

Dated: April 23, 2012

P.O. Box 980  
Valley Forge, PA 19482  
(610) 407-0700

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